

PATENT Docket No. SPLX.P0012

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on April 30, 2001.

Mani Adeli

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Steven Teig et al.

Serial No.: 09/737,245

Filing Date: 12/13/00

For: Method and Apparatus for Using

Connection Graphs with Potential Diagonal Edges to Model Interconnect

Topologies During Placement

Examiner: <not yet assigned>

Group Art Unit: 2811

SUBMISSION OF CORRECTED DRAWINGS

Box Missing Parts Assistant Commissioner for Patents Washington, D.C. 20231

Dear Sir:

This Submission of Corrected Drawings is submitted in conjunction with the response to the Notice to File Missing Parts of Application, which required the Applicants to submit formal drawings.

Applicants have submitted twenty-eight (28) sheets of formal drawings. Applicants have made minor corrections in three of these sheets for Figures 12, 14A and 14B. A red-lined version of these corrections accompanies this Submission of Corrected Drawings.

No new matter has been added in the three corrected sheets, as all the corrections are supported by the discussion in the specification. *See* page 27, lines 8-12 in reference to corrected Figure 12, page 32, lines 12-16 in reference to corrected Figure 14A, and page 32, line 19 to page 33, line 4 in reference to corrected Figure 14B.

Dated: April 30, 2001

By:

Registration No. 39,585

Respectfully submitted,

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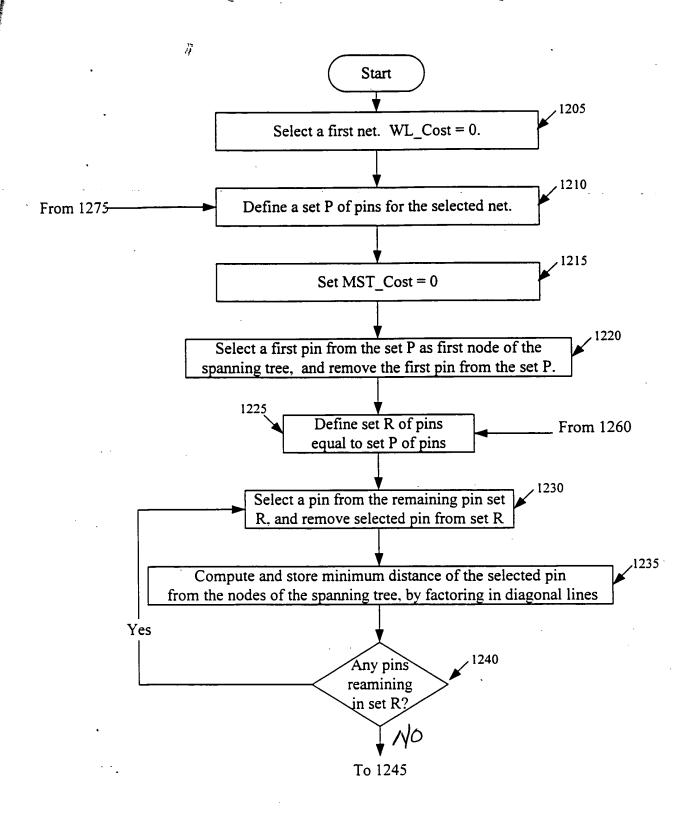


Figure 12A

Figure 12: Figure 12A Figure 12B

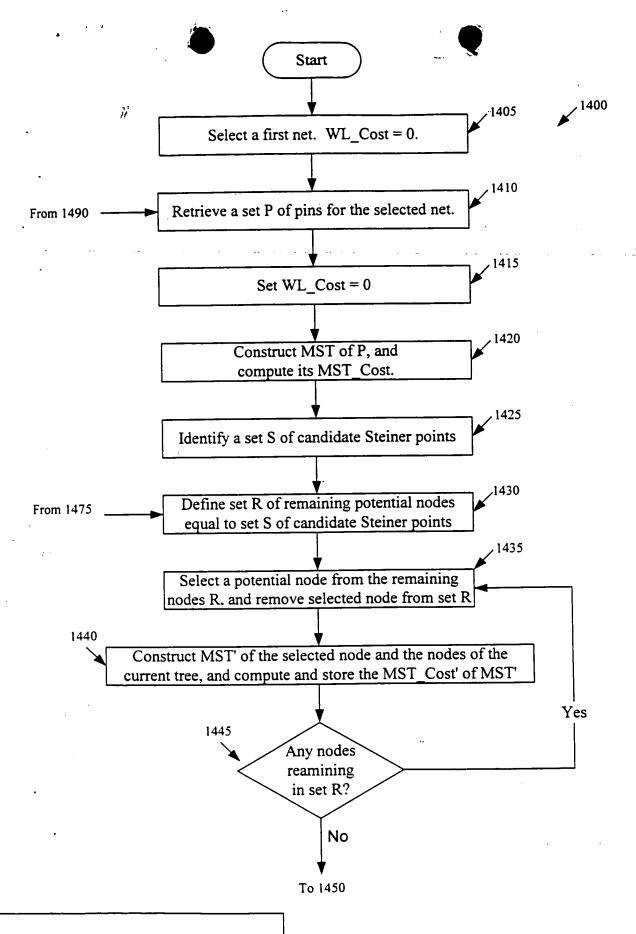


Figure 14A
Figure 14A
Figure 14A

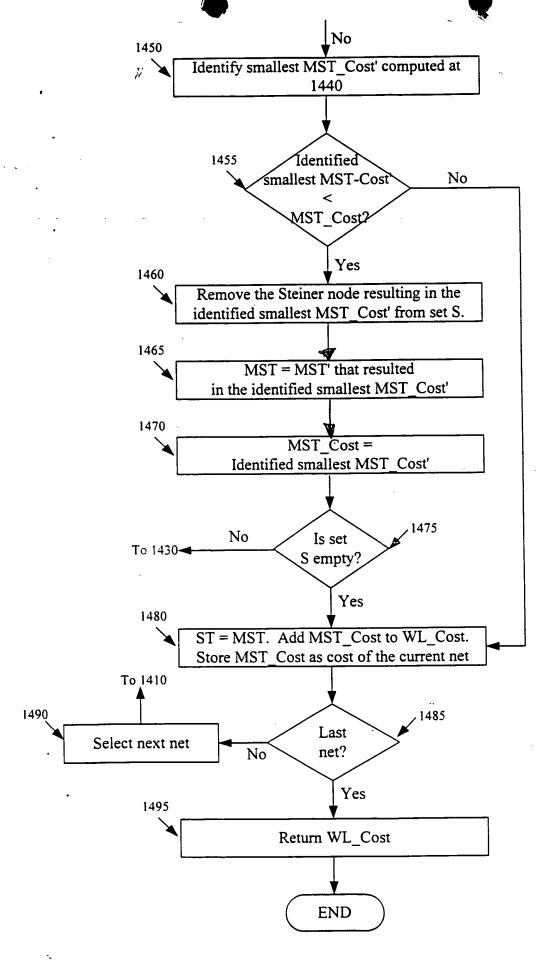


Figure 14B